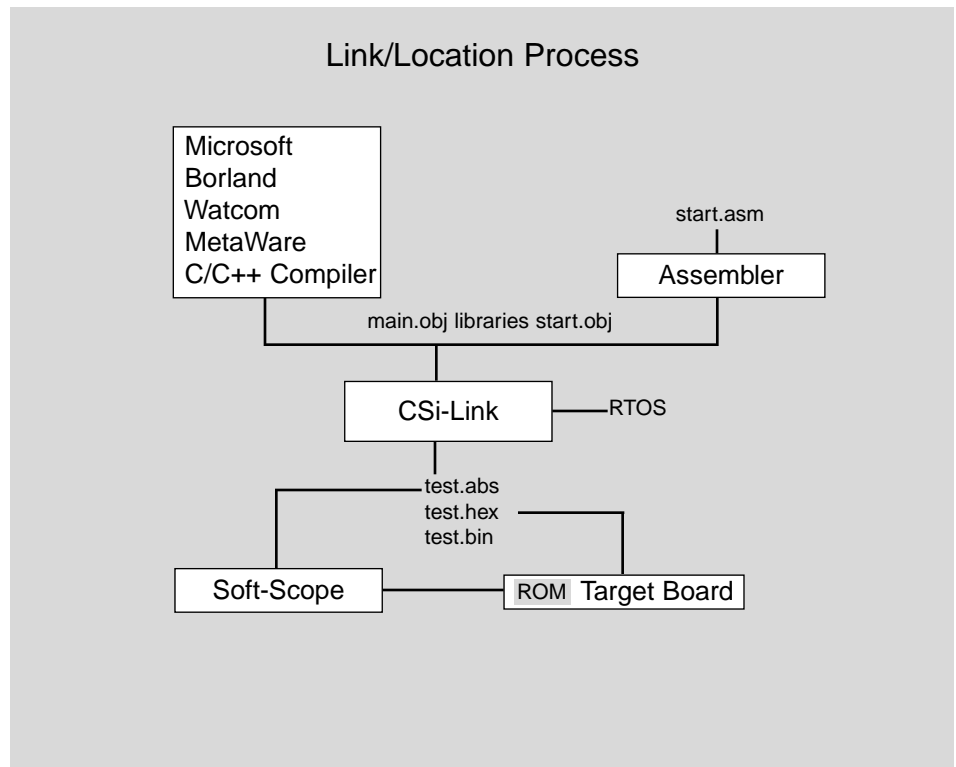# CSi-Link* Embedded Application Linker/Locator

- Supports C/C++ Compilers from Microsoft, Borland, Metaware, and Watcom
- Supports Microsoft and Borland Assemblers
- Generates OMF386 or OMF86 Files for Use with Soft-Scope and HEX or Binary Files for EPROM Programming
- Supports Mixed-Mode Applications
- Creates 16- and 32-Bit Protected-Mode CPU Structures GDT, IDT, LDT, Page Tables and TSS
- Packs RAM-Based Data for ROM Storage
- Source Code Provided to Unpack Data from ROM and Load Into RAM at Startup
- Sample Programs Including Romable Startup Code Provided

Link/Location Process

```
 ┌─────────────────┐
 │ Microsoft       │
 │ Borland         │                              start.asm
 │ Watcom          │                                 │
 │ MetaWare        │                         ┌───────────────┐
 │ C/C++ Compiler  │                         │   Assembler   │
 └─────────────────┘                         └───────────────┘
          │        main.obj libraries start.obj       │
          └──────────────┬──────────────────────────┘
                 ┌───────────────┐
                 │   CSi-Link    │────── RTOS
                 └───────────────┘
                         │   test.abs
                         │   test.hex
                         │   test.bin
          ┌──────────────┴─────────────┐
 ┌────────────────┐           ┌─────────────────────┐
 │  Soft-Scope    │───────────│ ROM   Target Board  │
 └────────────────┘           └─────────────────────┘
```

CSi-Link absolutely links and locates in memory your embedded Intel architecture application. It accepts your DOS and Windows executable files for debugging with Soft-Scope, available separately, and burning into EPROM. It accepts .EXE and .MAP files from Microsoft, Borland, Watcom, or MetaWare C/C++ compilers and generates OMF386 or OMF86 files for use with Soft-Scope and HEX or binary files for EPROM programming.

CSi-Link supports multiple mode applications and constructs 16- and 32-bit protected-mode CPU structures. These include GDT, IDT, LDT, page tables and TSS. CSi-Link supports RAM based variables by compressing the specified data and loading it into ROM. The package contains source code for decompressing this data and copying it into RAM at boot-up time.

PROCESSORS SUPPORTED:
Entire Intel Processor Family

DEVELOPMENT PLATFORMS:
IBM-PC running Windows*, Windows 95, Windows NT and DOS

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CSi

Concurrent Sciences, Inc.